



Please type a plus sign (+) inside this box → [+]

AUG 15 2001

PTO/SB/21 (08-00)

Approved for use through 10/31/2002. OMB 0651-0031

U.S. Patent and Trademark Office: U.S. DEPARTMENT OF COMMERCE

Under the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it displays a valid OMB control number.

<b>TRANSMITTAL FORM</b> (to be used for all correspondence after initial filing)	Application Number	09/583,087	
	Filing Date	September 5, 2000	
	First Named Inventor	Shunpei YAMAZAKI	
	Group Art Unit	2815	
	Examiner Name	J. Jackson, Jr.	
Total Number of Pages in This Submission		Attorney Docket Number	0756-2203

**ENCLOSURES (check all that apply)**

<input type="checkbox"/> Fee Transmittal Form <input type="checkbox"/> Fee Attached <input type="checkbox"/> Amendment / Reply <input type="checkbox"/> After Final <input type="checkbox"/> Affidavits/declaration(s) <input type="checkbox"/> Extension of Time Request <input type="checkbox"/> Express Abandonment Request <input checked="" type="checkbox"/> Information Disclosure Statement <input type="checkbox"/> Certified Copy of Priority Document(s) <input type="checkbox"/> Response to Missing Parts/Incomplete Application <input type="checkbox"/> Response to Missing Parts under 37 CFR 1.52 or 1.53	<input type="checkbox"/> Assignment Papers (for an Application) <input type="checkbox"/> Drawing(s) <input type="checkbox"/> Licensing-related Papers <input type="checkbox"/> Petition <input type="checkbox"/> Petition to Convert to a Provisional Application <input type="checkbox"/> Power of Attorney, Revocation Change of Correspondence Address <input type="checkbox"/> Terminal Disclaimer <input type="checkbox"/> Request for Refund <input type="checkbox"/> CD, Number of CD(s) _____	<input type="checkbox"/> After Allowance Communication to Group <input checked="" type="checkbox"/> Amendment ad Response to Election Requirement
Remarks		

**SIGNATURE OF APPLICANT, ATTORNEY, OR AGENT**

Firm or Individual name	Eric J. Robinson, Reg. No. 38,285 Nixon Peabody LLP 8180 Greensboro Drive Suite 800 McLean, VA 22012
Signature	
Date	August 15, 2001

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to: Commissioner for Patents, Washington, DC 20231 on this date:

Type or printed name

Signature

Date

Burden Hour Statement: This form is estimated to take 0.2 hours to complete. Time will vary depending upon the needs of the individual case. Any comments on the amount of time you are required to complete this form should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, Washington, DC 20231. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: Commissioner for Patents, Washington, DC 20231.



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of )  
Shunpei YAMAZAKI ) Group Art Unit: 2815  
Serial No. 09/583,087 ) Examiner: J. Jackson, Jr.  
Filed: September 5, 2000 )  
For: ELECTRO-OPTICAL DEVICE AND)  
METHOD FOR MANUFACTURING)  
THE SAME )

Election  
13/B  
RECEIVED  
AUG 20 2001  
TO 2600 MAIL-ROOM

**AMENDMENT AND RESPONSE TO ELECTION REQUIREMENT**

Honorable Commissioner for Patents

Washington, D.C. 20231

Sir:

In response to the Office Action of June 15, 2001, please amend the subject application as follows:

**IN THE CLAIMS:**

Cancel claims 42 without prejudice to applicants' right to file a divisional application with respect thereto.

Please amend claims 21-32, 41, 43-44, 48, 55, 59, 61, 62, and 63 as follows:

sub 7<sup>+</sup> m  
C1 21. (Amended) An active matrix display device having a pixel portion and a driver circuit portion, said driver circuit portion including at least one pair of complementary p-channel and n-channel thin film transistors, and said pixel portion including at least one thin film transistor, each of said transistors comprising:

B1 a crystalline semiconductor island on an insulating surface, said semiconductor island having source and drain regions and a channel region;  
a gate insulating film adjacent to at least said channel region;  
a gate electrode adjacent to said gate insulating film; and  
a leveling film covering each of said thin film transistors in both of the pixel portion and a part of the driver circuit portion,